

Docket No.: L&L-I0225

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : BURKHARD BECKER
Filed : CONCURRENTLY HEREWITH
Title : METHOD AND CIRCUIT CONFIGURATION FOR
TRANSMITTING DATA BETWEEN A PROCESSOR AND A
HARDWARE ARITHMETIC-LOGIC UNIT

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U.S. Patent No. 5,539,780 (Dutkiewicz), dated July 23, 1996;

U.S. Patent No. 5,905,757 (Kundmann et al.), dated May 18, 1999;

U.S. Patent No. 6,195,782 B1 (Rahmatullah et al.), dated February 27, 2001;

U.S. Patent No. 5,537,445 (Blaker et al.), dated July 16, 1996;

European Patent Application EP 0 899 887 A2 (Maru), dated March 3, 1999;

European Patent Application EP 0 590 597 A2 (Asano et al.), dated April 6, 1994;

Tietze, U. et al.: "Halbleiterschaltungstechnik, 11.2.2 RAM als Schieberegister", Springer Verlag, Vol. 10, 1993, pp. 284-285 and English translation "Electronic Circuits, 11.2.2 RAMs as Shift Registers", Springer Verlag, Vol. 9, 1991, pp. 284-285;

International Search Report, dated September 16, 2002.

As per the Notice in 1273 OG 55 (August 5, 2003) no copies of any above-mentioned U.S. patents and U.S. patent application publications are submitted for any application filed after June 30, 2003.

Respectfully submitted,



For Applicant

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Date: December 8, 2003

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/nt/kf

FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))				Attorney Docket No.: L&L-I0225 Appl. No.: Applicant: BURKHARD BECKER Filing Date: December 8, 2003 Group Art Unit:			
EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A	5,539,780	7/23/96	Dutkiewicz			
	B	5,905,757	5/18/99	Kundmann et al.			
	C	6,195,782 B1	2/27/01	Rahmatullah et al.			
	D	5,537,445	7/16/96	Blaker et al.			
	E						
	F						
	G						
	H						
	I						
FOREIGN PATENT DOCUMENT							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
	J	0 899 887 A2	3/3/99	Europe			
	K	0 590 597 A2	4/6/94	Europe			
	L						
	M						
	N						
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
		Tietze, U. et al.: "Halbleiterschaltungstechnik, 11.2.2 RAM als Schieberegister", Springer Verlag, Vol. 10, 1993, pp. 284-285 and English translation "Electronic Circuits, 11.2.2 RAMs as Shift Registers", Springer Verlag, Vol. 9, 1991, pp. 284-285;					
EXAMINER				DATE CONSIDERED			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							